A High-Efficiency, Simple-Structure, Compact Wideband Microwave Energy Harvester for Wirelessly Powered IoT Receivers

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Abstract—Wireless data links and wireless power transfer (WPT) have become emerging topics in IoT applications. A simple-structure high-efficiency rectenna is the key technology for wirelessly powered IoT receivers. Herein, we explore a unified codesign technique that synergizes wideband circular polarization (CP) antennas with high-frequency Gallium Arsenide (GaAs) Schottky diodes across the 10-20-GHz range. Our approach involves a systematic study of the rectifier's nonlinear behavior over wide frequency ranges, followed by the identification of an effective antenna candidate for co-design. This strategy effectively obviates the need for matching networks, filters, and extra components typically found in conventional wideband rectennas. Consequently, we present a co-design example: the proposed CP antenna, featuring a measured impedance bandwidth of 11.65-16.9 GHz (36.8%), a 3-dB axial ratio bandwidth (ARBW) of 12.55-16.9 GHz (29.54%), and a measured peak gain of 8.5 dBic. This design integrates CP magneto-electric (ME)-dipole units with rectifier topologies. Measured results show an RF-DC efficiency of over 50% within the 11-14.6 GHz (28%) and 10-18 dBm power range, with a maximum conversion efficiency of 61%. This design approach holds broad applicability for all wideband rectennas, offering notable advantages in terms of simplicity, efficiency, and compactness.

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I. Introduction

THE EXTENSIVE development of mobile communication has enriched the spectrum resources available in the environment. Utilizing radio frequency (RF) resources in space to convert them into electrical energy for device charging has become a hot research topic. This approach not only effectively leverages the excess RF resources in space but also addresses the cost issue associated with frequent battery replacement. Furthermore, it is challenging to continuously power some implantable, portable, and other devices with wires, and the development of wireless power transmission (WPT) has brought new solutions to these challenges. Rectennas are crucial components in the design of WPT systems, as they receive RF energy from free space and convert it into direct current (DC) power to charge devices. This technology is particularly impactful for Internet of Things (IoT) applications, where many devices, such as sensors, wearables, and medical implants, require reliable and continuous power without the inconvenience and cost of frequent battery replacements.

In IoT applications, WPT can significantly enhance device longevity and performance by providing a steady and sustainable power source [1], [2]. For instance, in smart home systems, various sensors and devices can be powered wirelessly, ensuring seamless operation and reducing maintenance. In healthcare, implantable medical devices can benefit from wireless power, eliminating the need for invasive surgeries to replace batteries. Additionally, industrial IoT applications can leverage wireless power to enhance the systems in challenging environments [3], [4], [5], [6].

The wideband rectenna demonstrates robustness to signal frequency variations due to its ability to operate over a wide frequency range. In comparison with single frequency rectennas, it exhibits the capability to capture a broader spectrum of RF energy, thereby enhancing energy harvesting (EH) efficiency. Furthermore, it facilitates simultaneous charging of devices operating in different frequency bands, leading to improved charging efficiency. Not only does the wideband rectenna obviate the need for multiple rectenna designs for different frequency bands, but it also contributes to cost reduction in the overall wireless energy transfer

system. There, in recent years, wideband rectennas have shown promising applications in WPT and frequency-scanning multinode tracking [7], [8], [9]. Within these applications, utilizing circular polarization (CP) transceivers can effectively mitigate polarization mismatches and power loss in far-field scenarios. However, the development of wideband CP rectennas above sub 6G bands remains limited, primarily due to challenges in efficiency, axial ratio, impedance matching, and the nonlinearity of Gallium Arsenide (GaAs) Schottky diodes across a broad spectrum, ranging from 10 to 20 GHz.

Compared with high-frequency rectennas, multiband and wideband low-input power rectennas operating at sub-6 GHz have been devised to meet the growing need for ambient EH from radio waves, benefit for applications like battery-less IoT sensors and self-sustaining low-power devices [10], [11], [12], [13], [14], [15], [16], [17], [18], [19]. In high-frequency ranges, however, rectennas face a significant challenge due to the loss associated with PCB microstrip lines. This makes it crucial to implement co-design strategies already realized in the low frequency for wideband rectennas design which can eliminate the need for separated impedance matching and filtering components and thereby enhance the overall efficiency [20], [21], [22], [23], [24]. However, the highfrequency GaAs diodes present distinctly different nonlinear impedance variations over a much broader frequency range compared to the low frequency [25], posing significant challenges in developing effective approaches for high frequency rectenna co-design. In [24], a miniaturized traveling wave antenna array is co-designed with a small rectifier to achieve stable DC output power. However, the bandwidth of this rectenna is limited to 8.3%, and the measured maximum RF-DC conversion efficiency is only 50%. In addition, as previously discussed, wideband, high-gain CP antennas/arrays are particularly suitable for WPT systems. Hence, various CP rectennas in the sub-6G-band have been proposed for WPT systems [26], [27], [28], [29], [30], [31]. In contrast, the above sub-6G counterpart still lacks sufficient research. Only a few CP rectennas have been realized in [32], [33], [34], [35], and [36]. For instance, [34] developed a CP substrate integrated waveguide (SIW) cavity-backed antenna array with a band range of 24.2–25.8 GHz, but only achieving a maximum measured conversion efficiency of 42%. CP antennas in the Ku band have been documented, but their integration with rectifier circuits remains unexplored, as indicated in [37], [38], [39], and [40]. Reference [38] describes a wideband CP antenna achieved through the use of cross dipoles and quadruple parasitic elements; however, the antenna's axial ratio bandwidth (ARBW) is limited to only 0.1%. A wideband CP antenna is effectively realized in [39] using an ME-dipole, but the resulting antenna structure is relatively large. While the performance of CP rectennas has been notably advanced in [33], [34], [35], and [36], these designs are typically characterized by large sizes, complex structures, or lower efficiencies. For instance, [33] employs a complex three-layer Fabry– Perot (FP) resonator antenna combined with a rectifier circuit for WPT. In [34], an SIW back-cavity CP antenna array is used to optimize CP performance; however, the associated rectifier circuit requires additional matching branches, further complicating the structure and increasing its size. In [35], WPT is effectively achieved in a dual-band configuration; however, the complex SIW antenna array significantly enlarges the rectenna's size. In [36], although a higher conversion efficiency is attained, the bandwidth for over 50% conversion efficiency is limited to only 2%, and the proposed CP metasurface antenna structure is notably complex. Consequently, there is a pressing need to develop design strategies that not only broaden the operating bandwidth of high-frequency rectennas but also simplify their structure, reduce their size, and enhance their RF-to-DC efficiency.

In this article, we introduce a simple structure, compact, high-efficiency, wideband CP rectenna, co-designed for 10-20 GHz. Specifically, we utilize CP ME-dipole as the antenna unit to adapt the impedance nonlinear variation of the high-frequency Schottky diode MA4E1317, eliminating the losses associated with wideband matching networks operating in the high-frequency band of 10–20 GHz. Consequently, our co-designed CP rectennas demonstrate state-of-the-art performance, including a measured wide impedance bandwidth of 33.4%, the measured RF–DC conversion efficiency of up to 61%, peak gain of approximately 8.5 dBic for the CP antenna, and a beamwidth exceeding 60°.

The remainder of this article is organized as follows. Section II details the structure, operating principle, and results of the proposed wideband CP antenna. Section III discusses the co-design and working principle of the wideband rectifier. Experimental validation of the designed rectenna is presented in Section IV. Finally, Section V provides a summary of this article.

II. CP ANTENNA DESIGN FOR RECTENNA CO-DESIGN

A. Why Circular Polarization?

As an important part of the WPT system, the selection of the antenna type is extremely important. Linearly polarized (LP) receiving antennas typically impose stringent requirements on

the transmitting antenna. In practical scenarios, electromagnetic wave propagation can lead to deflection of polarization direction through reflection and refraction. When the polarization direction of the LP receiving antenna deviates from that of the transmitting antenna, signal reception diminishes accordingly. However, for CP antennas, the received signal remains consistent regardless of the polarization direction of the transmit antenna. This characteristic effectively suppresses multipath interference and mitigates issues related to polarization mismatch. As a result, CP antennas are capable of providing a more stable DC output voltage [41]. The advantages of CP antennas become even more significant in IoT multinode charging applications. In these scenarios, multiple devices are often scattered over a wide area, each requiring reliable and consistent power delivery. CP antennas ensure that all devices, regardless of their orientation or position relative to the transmitting antenna, receive a consistent signal. This uniformity simplifies the design and deployment of WPT systems for IoT networks, as it reduces the need for precise alignment between transmitters and receivers [42], [43]. To clearly demonstrate the performance advantages of CP antennas compared to LP antennas, the performance comparison is listed in Table I [41], [44]. This table clearly illustrates the

Harvested DC

Application

flexibility

Performance	CP Receivers	LP Receivers Linear polarization Significant		
Polarization characteristic	All polarization			
Multipath effect	Low			
TX and RX alignments	No alignment is needed	Require precise alignment		
Hamasta I DC	Relatively	T.T.,		

stable

High flexibility\

wide coverage

Unstable

Low flexibility\ small

coverage

TABLE I

COMPARISON OF PERFORMANCE OF CIRCULARLY POLARIZED AND LP

ANTENNAS

superior performance of CP antennas in terms of maintaining consistent signal reception, reducing interference, and providing stable DC output voltage. These advantages make CP antennas a more reliable choice for WPT systems, particularly in environments where signal polarization can be affected by various factors. Dual-linearly polarized (Dual-LP) antennas typically require two separate input ports with high isolations. They also necessitate precise alignment of the polarization directions between the transmit and receive antennas to maintain efficiency, with any mismatch leading to significant reductions in receive efficiency. In contrast, the proposed CP antenna requires only one input port, which facilitates integrated design with the rectifier and supports the development of miniaturized rectennas. Furthermore, integrating a Dual-LP antenna with a rectifier circuit requires separate processing of the two polarized signals. This often necessitates dualchannel rectifying circuits or additional combiners, thus adding to the complexity of circuit design and fabrication. In highfrequency applications, a Dual-LP antenna typically needs at least two rectifiers, potentially enlarging the overall design and introducing additional losses. Therefore, for simplicity, integration ease, and reduced system complexity, a singleport CP antenna is chosen for this study. In IoT applications, where numerous devices may be deployed in diverse and dynamic environments, the reliability and simplicity offered by CP antennas can significantly enhance the efficiency and effectiveness of WPT. This leads to more robust and scalable IoT networks, capable of supporting a wide range of devices with minimal maintenance and configuration.

B. Antenna Configuration

ME-dipole antennas, enhanced with the integration of complementary source technology, have demonstrated effective CP, along with a wide impedance bandwidth and high gain. These features make them highly suitable for applications in the microwave bands, as evidenced in [45], [46], [47], [48], [49], and [50].

Song et al. [8] have introduced the use of complementary source technology to achieve broadband CP performance. However, the working principle of the antenna was not

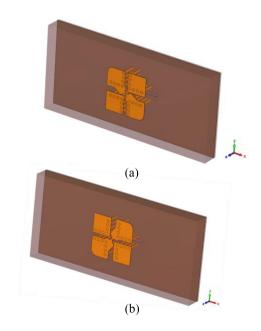
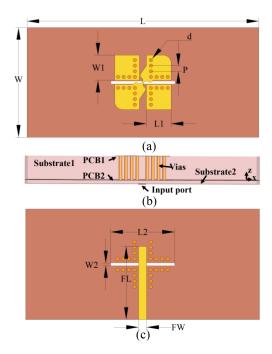


Fig. 1. Proposed wideband CP antenna. (a) LHCP. (b) RHCP.

detailed, nor was its performance verified in real-world scenarios. To address these gaps, we have conducted further research within the satellite communication band and potential 5G communication bands. This research includes a detailed explanation of the antenna's design process and working principles, along with rigorous testing of the antenna's performance as a receiver. In addition, the frequency has been scaled down in order to compromise the tradeoffs between device size and power loss of propagation. The proposed designs for the wideband left-handed circular-polarized (LHCP) and right-handed circular-polarized (RHCP) dipole antennas are depicted in Fig. 1. This article specifically focuses on the RHCP antenna to elucidate the operating principles of these antennas. The 3-D view of this antenna is presented in Fig. 2. Rogers RT 5880 with $\varepsilon_r = 2.2$, $\tan \delta = 0.0009$ was adopted as the two substrates (substrate 1 and substrate 2) with different thicknesses. Two symmetrical metal patches are printed on the top layer of substrate 1, where a pair of metal patches is diagonally connected. 4×7 metalized vias are placed in substrate 1, and the diameter of their metal vias is 0.52 mm. The electric (E) and magnetic (M) dipoles of the antenna are composed of two pairs of symmetrical metal patches and a transverse slot, respectively. For feeding the antenna, a microstrip is printed on the underside of substrate 2, while a transverse slot is etched on the top of this substrate to excite the radiation structure. Slot feeding, in comparison to probe feeding, simplifies the complexity of the rectifier in system integration. Electromagnetic waves are coupled from the feed port to substrate 1 through the etched transverse slot in the middle, thereby exciting the E- and M-dipoles. The design and simulation of the proposed antenna were carried out using commercial software—CST Microwave Studio 2021. The thicknesses of PCB substrates 1 and 2 are 3.175 and 0.508 mm, respectively, and the copper layer is 0.036-mm thick. To minimize fabrication costs, the two substrates are produced separately and then assembled using nylon (plastic)



Geometry of the proposed RHCP antenna. (a) Top view. (b) Side view. (c) Back view.

TABLE II DIMENSIONS OF RHCP ANTENNA (UNIT: MM)

Parameters	Value	Parameters	Value	
W	18	L_2	10.4	
$W_{_{I}}$	4.12	FL	11.9	
W_2	0.4	FW	1.3	
L	38	d	0.56	
L_{I}	4.05	p	0.92	

screws. The final optimized geometric dimensions of the proposed CP antenna are detailed in Table II.

C. Operating Principle of CP Radiation

The inclusion of a complementary source radiation in the antenna design introduces an additional resonant point, which serves to broaden the impedance bandwidth and enhance impedance matching within the operating band. As illustrated in Fig. 2, this configuration consists of four rectangular patches (two of which have one corner shaped into an arc), a metal strip with two circular arcs removed, and a pattern of 4×7 metalized vias. The four rectangular patches in conjunction with the transverse slot function as an E-dipole and an M-dipole, respectively. For CP, it is necessary to have two orthogonal LP modes of equal amplitude and a phase difference. Following the complementary sources concept. when the E-dipole and M-dipole are aligned parallel to each other, they can generate two orthogonal electric field components [45]. However, in this design, the transverse slot can only excite vertical E-dipole and parallel M-dipole, which together form LP ME-dipole [41]. To achieve CP, it is essential to excite another LP ME-dipole mode in the orthogonal direction. To this end, we incorporate a metal strip with removed circular arcs, which connects two of the rectangular patches, each having a corner in the shape of a circular arc. Based on the above ideas, the proposed ME-dipole antenna can well achieve wideband CP radiation. According to the concept of complementary sources, an E-dipole and an M-dipole with the same amplitude and phase difference, which are parallel to each other, can obtain two orthogonal electric field components [51], [52], [53]. The directions of the E- and M-dipoles are assumed to be along the direction, where the M-dipole can be regarded as a small ring antenna with area A_0 and its plane is perpendicular to the M-dipole. The far field of the E-dipole and the far field of the M-dipole can be expressed as follows [54]:

$$E_x = j \frac{60\pi I}{r} \frac{L}{\lambda} \sin \theta e^{-jkr} \tag{1}$$

$$E_x = j \frac{60\pi I}{r} \frac{L}{\lambda} \sin \theta e^{-jkr}$$

$$E_y = j \frac{120\pi^2 I}{r} \frac{A_0}{\lambda^2} \sin \theta e^{-jkr}$$
(2)

where L is the length of the E-dipole, I is the current, r is the radiation distance, and λ is the wavelength in free space. Therefore, when the E-dipole parallel to each other are excited with the same amplitude as the M-dipole, E_x and E_y have a phase difference. Furthermore, the values of E_x and E_y mainly depend on L and A_0 , respectively. By adjusting the parameters to make the values of E_x and E_y equal, CP radiation is realized. The surface current distribution of the proposed CP antenna at 14.5 GHz is shown in Fig. 3, with RHCP as an example, to explain the operation of the antenna. At t =0 and t = T/2, where T is the period of the oscillation, the surface currents on the four patches are mainly distributed along the diagonal direction, which indicates that the E-dipole in the diagonal direction is excited, and the amplitude of the current is sinusoidal distribution along this direction. At the same time, M-dipole along the diagonal direction is also excited. Similarly, at t = T/4 and t = 3T/4, the surface current on the patch is mainly distributed along the direction, where the E-dipole in this direction is excited at the same time as the M-dipole. The ME-dipole in these two directions is excited with a time delay of T/4, which corresponds to a phase difference equal to 90°. Therefore, CP radiation of this design is realized.

D. Antenna Performance

In order to verify the performance of the antenna, the antenna was fabricated, and the prototype of the fabricated antenna is shown in Fig. 4(a) and (b). Simulation and measurement of reflection coefficient $|S_{11}|$ is shown in Fig. 4(c). The results show that the proposed antenna can be used at 11.91-17.12 GHz ($|S_{11}| < -10 \text{ dB}$) with a simulated wide impedance bandwidth of 35.9%. The measured fractional impedance bandwidth is 36.8%, which covers 11.65-16.9 GHz ($|S_{11}| <$ -10 dB). Fig. 4(d) shows the measured and simulated 3-dB ARBW and gain results of the proposed antenna. It is shown that the simulated 3-dB ARBW of the proposed antenna ranges from 12.6 to 16.63 GHz, and 27.6% of the wide ARBW is achieved. The measured 3-dB ARBW ranges from 12.55 to

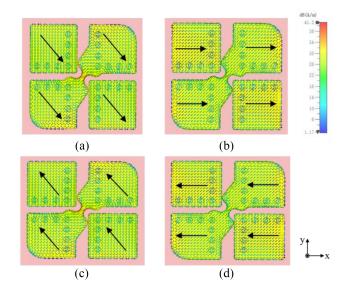


Fig. 3. Current distribution at 14.5 GHz of the proposed CP antenna at (a) t = 0, (b) t = T/4, (c) t = T/2, and (d) t = 3T/4.

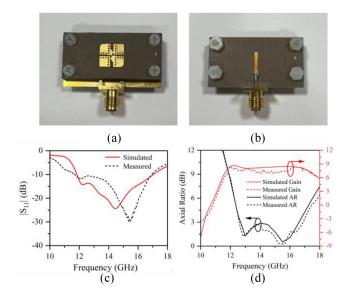


Fig. 4. Proposed CP ME-dipole antenna. (a) Top view of fabricated prototype. (b) Back view of fabricated prototype. (c) Simulated and measured $|S_{11}|$. (d) Simulated and measured AR and gain.

16.9 GHz (29.54%). Moreover, the band of the 3-dB axis ratio is in the impedance bandwidth, the overlap bandwidth reaches 29.54%, so it has a significant advantage in the wideband utilization. The simulated and measured peak gain of the proposed antenna is 8.7 and 8.5 dBic, respectively. Fig. 5 plots the simulated and measured radiation patterns for the CP antenna at 12.5, 14.5, and 16.5 GHz with $\varphi = 0^{\circ}$ and $\varphi = 90^{\circ}$. As shown in the figure, the measured results of the radiation pattern are generally consistent with the simulated results. The cross-polarization level is less than -15 dB, and the half-power beam bandwidth (HPBW) of this antenna is more than 60°. As the effective aperture of an antenna, which is a function of its gain, is proportional to the power it can harvest. Conversely, the beam width is related to spatial coverage, crucial for capturing power from wider incident angles. To balance the

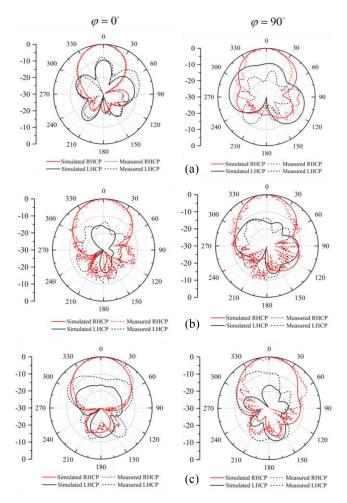


Fig. 5. Simulated and measured radiation patterns in $\varphi=0^{\circ}$ and $\varphi=90^{\circ}$ at (a) 12.5 GHz, (b) 14.5 GHz, and (c) 16.5 GHz.

tradeoffs between high gain and wide beam, we have chosen the ME-dipole antenna as the receiver due to its excellent combination of relatively wide beamwidth and high gain.

It can be observed from the results in Fig. 5 that the radiation pattern is distorted. The primary cause of this distortion is the inclusion of an SMA connector in the CST simulation of the CP antenna. Specifically, during the simulation process, both the CP antenna and the SMA connector are simulated together. The proximity of the SMA to the side of the antenna generates electromagnetic waves that lead to asymmetric radiation of the proposed CP antenna. The resulting pattern is illustrated in Fig. 6. The discrepancy between the simulated and measured results can be attributed to errors between fabrication, measurement, and alignment of the measuring device. The above results show that the antenna in this frequency band has the characteristics of low impedance bandwidth, high gain, wide HPBW, and wide 3-dB ARBW. Therefore, the proposed CP MEdipole antenna based on complementary source technology operates in 10-20 GHz, which verifies the correctness of the proposed structure theory, and the structure has good radiation performance. Table III presents a performance comparison between our proposed CP antenna and other CP antennas previously reported in the literature. Although previous efforts

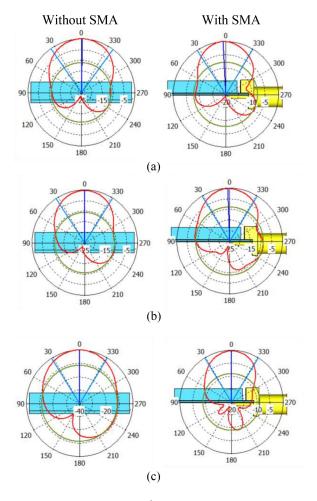


Fig. 6. Radiation pattern at $\varphi=90^\circ$ for CP antenna with and without SMA in CST simulation. (a) 12.5 GHz. (b) 14.5 GHz. (c) 16.5 GHz.

have demonstrated good performance, they also exhibit some drawbacks. References [33] and [34] attempted to enhance the gain and impedance bandwidth of CP antennas using frequency-selective surface (FSS) coverage and by integrating slots on the patch, respectively. However, their 3-dB ARBW is restricted to only 10%. Similarly, the 3-dB ARBW for the antennas in [37] and [39] is also limited to less than 10%. In [37] and [38], cross dipole and ME-dipole antennas are employed to achieve a wider impedance bandwidth, respectively. Yet, these designs rely on coaxial feeders, complicating the integration with planar rectifier circuits and necessitating connections via $50-\Omega$ SMA connectors. This approach not only increases the rectenna's overall size but also escalates costs. In [40], a dielectric resonator antenna was proposed, which achieved a better impedance bandwidth of more than 50%, but it had a narrow 3-dB ARBW and a low gain of only about 6 dBic in the operating band. The stacked antenna approach in [55] sees a 20% improvement in 3-dB ARBW. However, its multilayer structure adds to the complexity and manufacturing costs. In [32], the impedance bandwidth of a CP antenna is enhanced using an ME-dipole antenna with complementary source technology, but this bandwidth remains below 30%. Additionally, the use of SIW for feeding further complicates the overall antenna structure. In contrast

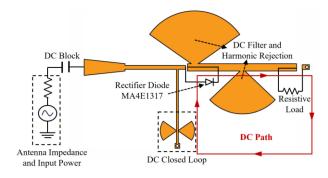


Fig. 7. Layout of the proposed rectifier.

to these reported designs, our proposed antenna offers several advantages, including a simpler structure, a wider impedance bandwidth, an expanded 3-dB ARBW, and a broad overlap bandwidth.

III. CO-DESIGN WIDEBAND RECTIFYING CIRCUIT

The integration of a high-efficiency, wideband rectifier with the proposed wideband CP antenna is key to realizing the wideband characteristics of the overall system. The layout of the rectifier is depicted in Fig. 7. This structure is printed on a Rogers RT 5880 substrate with a thickness of 0.508 mm. The circuit model primarily consists of a shunt circuit, a high-frequency rectifier diode, a DC pass filter (fan-shaped stub), and a load resistor. The main topology features a DC filter placed in front of the load resistor, serving as an output filter. This filter can substitute the bypass capacitor, not only suppressing the second and third harmonics but also smoothing the voltage ripple at the DC output. This design enhances harmonic suppression, resulting in a rectification process characterized by minimal distortion and low loss. The overall design of the rectifier is relatively straightforward, eliminating the need for an additional impedance matching network. The feed port employs a conical line to facilitate the transmission of wideband impedance in the real part. A shunt circuit, positioned between the antenna and the diode, completes the DC closed loop. Functioning effectively as an inductance, its impact on impedance matching is minimal. The parameters of the ME-dipole antenna are fine-tuned to adjust the antenna impedance. Consequently, the impedance of the rectifier dynamically aligns with the antenna's impedance, ultimately achieving optimal conjugate matching within the operating band.

The selection of the rectifier diode is crucial in the overall performance of the rectifier circuit. In this study, we utilize GaAs Flip Chip Schottky Barrier Diodes, specifically the MA4E1317 model, which are capable of operating at frequencies up to 80 GHz [57]. These diodes are particularly well suited for rectifier designs that require a broad range of power and frequency.

To accurately represent the performance of the MA4E1317 diode, we use its equivalent model parameters. These parameters are extracted from the measurements of current–voltage (I-V), capacitance–voltage (C-V), and small-signal S parameters. This approach enhances the consistency between the

TABLE III
COMPARISON BETWEEN THE PROPOSED ANTENNA AND OTHER WIDEBAND CP ANTENNA

Ref.	Year	No. of Layer	Antenna type	Frequency (GHz)	RLBW (%)	ARBW (%)	PG (dBic)	Is it measured?	Size (λ_0^3)
[32]	2023	2	Complementary antenna	33.87-43.15	25.7	24.8	10.33	No	1.156*1.136*0.267
[33]	2016	3	FP metasurface antenna	33.6-37	10	6.5	17	Yes	3.7*3.7*0.2
[34]	2014	2	SIW cavity- backed antenna	24.2-25.8	6.4	4.5	10.3	Yes	2.5*2.5*0.085
[37]	2023	1	Crossed dipole antenna	15.5-23	38.9	0.1	NA	Yes	0.68*0.68*0.22
[38]	2023	2	ME-dipole antenna	9.26-13.14	34.6	34.6	NA	No	2.33*2.33*0.09
[39]	2021	1	Hybrid choke antenna	14.68-15.39	4.7	0.6	10.2	No	1.8*1.8*0.054
[40]	2024	1	Dielectric resonator antenna	8.26-14	51.57	11.01	8.1	Yes	0.68*0.68*0.096
[55]	2018	3	Stacked patch antenna	25-32.6	25.5	17.5	≈11.5	No	1.78*1.72*0.1
[56]	2018	2	Microstrip antenna	39.2-50.4	24.9	17.3	≈8.5	No	1.72*1.72*0.15
This Work	2024	2	ME-dipole antenna	11.65-16.9	36.8	29.54	8.5	Yes	1.3*1.83*0.15

RLBW: -10 dB Return loss bandwidth. ARBW: 3 dB axial ratio bandwidth. PG: peak gain.

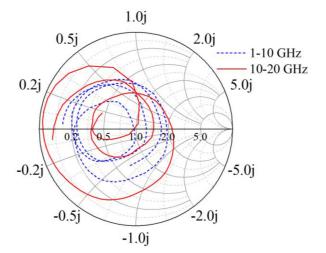


Fig. 8. Simulated impedance characteristic variation of the diode in the low frequency and high frequency.

diode's behavior in the designed circuit and its performance during testing [58]. The MA4E1317 diode offers low loss and high conversion efficiency, attributed to its small series resistance (4 Ω) and low zero-bias junction capacitance (0.02 PF). Furthermore, its high reverse breakdown voltage (7 V) ensures robustness against breakdown at high input power.

In order to analyze the impedance variation of the MA4E1317 diode, the impedance characteristic variation of the diode (the load resistance was set to the optimal value of 100 Ω) in the low-frequency band (1–10 GHz) and the high-frequency band (10–20 GHz) was simulated in the ADS, and the results are shown in the smith impedance variation circle chart of Fig. 8. The resulting figure shows that the high-frequency GaAs diode MA4E1317 exhibits significantly different nonlinear impedance variations over a wider frequency range compared with the low-frequency selection, which poses a significant challenge for the high band antenna co-design method. At the same time, we changed the input power value of the rectifier when the frequency was maintained at 10-20 GHz, and obtained the impedance change characteristics of the diode under different input powers (see Fig. 9). It can be seen from the figure that the impedance of the diode changes with the power value, and its nonlinear characteristics are also affected. Therefore, according to the effective reference data provided by the impedance variation curve, we use the co-design method to realize the conjugate matching between the CP antenna and the rectifying circuit in the wide frequency range from 10 to 20 GHz.

An important design feature of this rectifier is the inclusion of two metalized vias, serving the shunt circuit branch and the DC terminal grounding, respectively. The rectifier's structure is

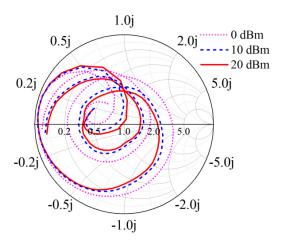


Fig. 9. Simulated impedance changes characteristics of the rectifier diode under different input powers.

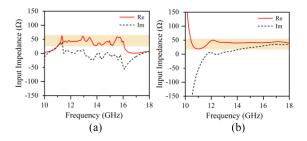


Fig. 10. Simulated input impedance. (a) Rectifier. (b) CP ME-dipole antenna.

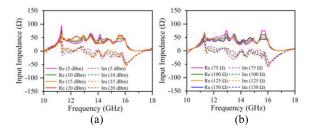


Fig. 11. Simulated impedance changes of the input port of the rectifier. (a) Different power values. (b) Different load values.

almost entirely solder-free, with soldering limited to the diodes and load resistors. This design choice effectively minimizes the adverse effects commonly associated with impedance matching networks and multiple solder joints.

The RF–DC conversion efficiency (η_r) of the rectifier can be calculated using the following formula:

$$\eta_r = \frac{V_{\rm DC}^2}{R_{\rm Load}} \times 100\% \tag{3}$$

where $V_{\rm DC}$ is the DC voltage output at both ends of the load, $R_{\rm Load}$ is the load resistance, and P_{in} is the input power of the rectifier. Finally, the feed port of the antenna is connected to the rectifying circuit input port for co-simulation.

CST and ADS software are used to co-simulate the antenna and rectifier. The impedance values of the receive antennas are extracted using CST and the SNP file is exported, which is then imported into ADS for co-simulation to describe the overall conjugate matching process. This process can approximate the integration of the receiving antenna and the

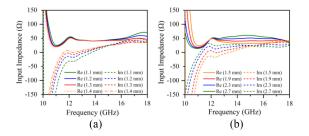


Fig. 12. Simulated impedance changes of the input port of CP ME-antenna. (a) Different *FW* values. (b) Different *FL* values.

rectifier, and the conjugate matching process can make the receiving antenna match the rectifier with the minimum loss. In detail, the input impedance of the final optimized CP ME-dipole antenna and rectifier (see Fig. 10) is shown here. Fig. 10(a) illustrates a gradual transition of the imaginary part value of the port impedance from negative to positive, indicating a shift in the antenna impedance characteristics from capacitive to inductive. Fig. 10(b) correspondingly reflects the change of rectifier port impedance from inductive to capacitive. Next, we show the influence of different parameters on the impedance of the rectifier and CP antenna. Subsequently, we demonstrate how the conjugate matching of the antenna and the rectifier is achieved, as shown in Figs. 11 and 12. It can be seen from Fig. 11 that the rectifier input impedance will change with the input power and load resistance. The optimal conjugate matching between the rectifier and the CP antenna can be realized by adjusting the input power, the load resistance value, and the circuit structure. Fig. 12 also demonstrates the impact of varying FW and FL values on the impedance of the CP antenna. Finally, by optimizing the rectifier and antenna structure parameters, the optimal conjugate matching is achieved when the input power is 18 dBm, the load resistance is 100 Ω , FW = 1.3 mm, and FL = 11.9 mm. Moreover, we also analyze the surface current distribution of the CP magnetoelectric dipole antenna at 12 and 16 GHz, as shown in Fig. 13. The figure shows that the antenna transitions from capacitive to inductive behavior as the frequency increases [42]. This transition reflects the trend of antenna port impedance variation, which is consistent with Fig. 10(b). After conjugate matching with the rectifier, the impedance comparison between the receiver antenna and the rectifier at some specific frequency points is listed in Table IV. For instance, at 14 GHz, the port output impedance of the receive antenna is $40+j15.9 \Omega$, and the port input impedance of the rectifier circuit is $26.6-j2.1 \Omega$. At 16 GHz, the output impedance of the receive antenna's port is $40.8+j29.7 \Omega$, while the input impedance of the rectifier circuit's port is $40.6-j44.4 \Omega$. This data demonstrates that the co-designed approach employed in this work successfully achieves optimal conjugate matching in the operating band. The congruence between the impedances of the antenna and the rectifier at various frequencies ensures effective power transfer and enhanced system performance, which is a crucial aspect of the design's efficacy.

The reflection coefficient $|S_{11}|$ and RF–DC conversion efficiency results of the final simulation of the rectenna after

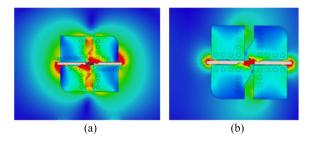


Fig. 13. Surface current distribution of the proposed CP ME-dipole antenna. (a) 12 GHz. (b) 16 GHz.

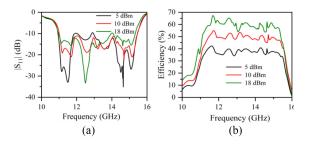


Fig. 14. Simulated (a) reflection coefficient and (b) RF-DC conversion efficiency at different power levels.

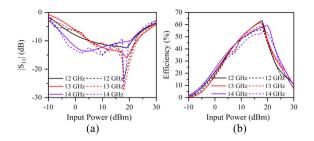


Fig. 15. Simulated (solid line) and measured (dashed line) reflection coefficient and RF-DC conversion efficiency at different frequencies.

TABLE IV SIMULATED INPUT IMPEDANCES

Freq	CP antenna	Rectifying circuit		
12 GHz	46.4+j6.1 Ω	43.5+j3.3 Ω		
13 GHz	42.3+j6.7 Ω	61.2+j11.3 Ω		
14 GHz	40.0+j15.9 Ω	26.6-j2.1 Ω		
15 GHz	41.2+j23.7 Ω	42.6-j6.9 Ω		
16 GHz	40.8+j29.7 Ω	40.6-j44.4 Ω		

ADS optimization are shown in Fig. 14. Note that here $|S_{11}|$ is calculated using the complex impedance of the receive antenna with the rectifier, not just the conventional 50 Ω . The frequency characteristics in the 10–16 GHz band show that the proposed rectenna has a good impedance matching from 11.05 to 15.4 GHz. Moreover, the simulated conversion efficiency is up to 67% when the input power is 18 dBm. When the input power in the operating band is 5, 10, and 18 dBm, the simulated conversion efficiency of the rectenna is 40%, 50%, and 60%, respectively. The conversion efficiency mentioned above is obtained when the load impedance is 100 Ω . We

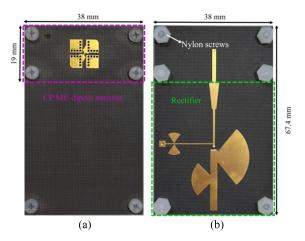


Fig. 16. Fabricated prototype of the proposed rectenna. (a) Top view. (b) Bottom view

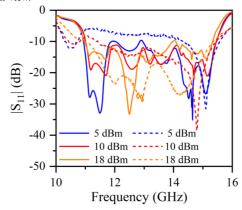


Fig. 17. Simulated (solid line) and measured (dashed line) reflection coefficient $|S_{11}|$ of rectifying circuit.

analyze the simulated and measured matching and RF-DC conversion efficiency for different input powers at 12, 13, and 14 GHz, respectively, as shown in Fig. 15(a) and (b). When the input power of the rectenna is in the range of $-2\sim22$ dBm, the impedance matching $|S_{11}|$ is less than -10 dB. The maximum simulated efficiency of the rectenna ranges from 50% to 67% when the input power is around 18 dBm at different frequencies. The conversion efficiency is close to 0 for an input power of -10 dBm. The measurement results are consistent with the simulation results. In conclusion, the impedance bandwidth of the proposed wideband rectenna covers the frequency band of 11.05–15.4 GHz ($|S_{11}| < -10 \text{ dB}$), the fractional bandwidth is 32.3%, and the simulated conversion efficiency in the operating band is greater than 50% (at an input power of 18 dBm). Moreover, the conversion efficiency is 30%-67% for input power of 5-18 dBm.

IV. FABRICATION AND MEASUREMENT OF RECTENNA

To test the performance of the proposed rectenna, the structure was fabricated using PCB technology as shown in Fig. 16. The purple marked in the figure is the proposed ME-dipole antenna with dimensions of $19\times38\times3.175$ mm³. Green indicates the rectifier, whose size is $38\times67.4\times0.508$ mm³ and is located on the top of the lower substrate. The feed port is connected to the feed line of the ME-dipole, and the

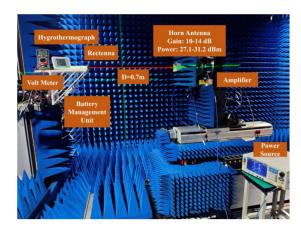


Fig. 18. OAT test of the wirelessly powered IoT sensors using the proposed rectenna inside an anechoic chamber.

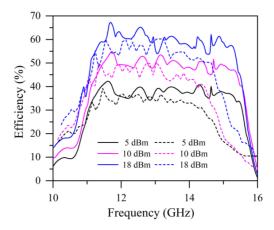


Fig. 19. Measured and simulated results of the proposed rectenna, solid lines represent simulation values and dashed lines represent test values: RF–DC conversion efficiency versus frequency under the input power of 5, 10, and 18 dBm.

position does not interfere with the CP ME-dipole antenna in the overlapping region. The two-layer PCB substrate does not require soldering or bonding. Six air holes are drilled along the edge of the substrate, and nylon screws are employed to secure it in place. To verify the impedance matching of the rectifier, the rectifier was tested independently, and its feed port was matched to a standard impedance of 50 Ω . The results of its simulated and measured reflection coefficient $|S_{11}|$ are shown in Fig. 17. According to the results in the figure, when the input power is 18 dBm, the reflection coefficient $|S_{11}|$ in the frequency band is better than -15 dB, which has a good impedance matching. Therefore, the correctness of the proposed rectifier structure is verified.

Next, to show the overall performance, we test the overall conversion efficiency of the rectenna. The experimental measurements setup of the rectenna system are displayed in Fig. 18. The entire system was evaluated in an anechoic chamber to ensure accurate and interference-free measurements. For the testing procedure, a signal generator, the Ceyer 1435D, was employed to generate RF signals within the range of 10-18 GHz. These RF signals were then amplified using Ceyear 80230F (0.5-40 GHz) broadband power amplifier. The signal is then transmitted through a 6-18 GHz horn antenna (HD-6018DRHA10S) with a gain of G_I , which radiates power

of P_t . The proposed rectenna with gain G_r is at distance D from the transmit antenna to receive the transmitted signal, where the broadside direction of the circularly polarized dipole is aligned with the horn, and the value of distance D is 0.7 m. The load of the rectenna is connected to a BQ25504 dcdc battery management unit (at the load resistor), which is used to stabilize the voltage output of the rectenna. The output port is connected to a thermohygrometer, which can display temperature and humidity values. The test process includes signal source sending signal to the transmitting antenna (horn antenna), rectenna harvesting energy from the horn antenna, and then converting RF energy into DC power through the rectifier, and then using the battery management unit to stabilize the voltage, and finally the thermohygrometer is displayed to successfully demonstrate the EH ability of the proposed rectenna. The received power of a rectenna can be expressed by the following formula:

$$P_r = P_t + G_t + G_r + 20 \log_{10} \left(\frac{\lambda}{4\pi D}\right)$$
 (4)

where P_r is the input RF power of the rectenna, P_t is the transmit power of the horn antenna, G_t is the realized gain of the horn antenna, G_r is the realized gain of the rectenna, λ is the wavelength in free space, and D is the transmission distance between the transmit antenna and the receive antenna.

Fig. 19 presents both measured and simulated results for the RF-DC conversion efficiency of the rectenna at different received powers, specifically at 5, 10, and 18 dBm. This figure demonstrates that the rectenna exhibits high RF-DC conversion efficiency within the targeted frequency band. The measured conversion efficiency is higher than 50% in the frequency band of 11–14.6 GHz, and the highest measured conversion efficiency can be as high as 61%. The consistency between the measured results and the simulated ones is notable, suggesting the effectiveness of the design and simulation process. However, some deviation between these results is observed, which may be attributed to factors, such as the soldering quality between the rectifier diode and the SMA joint, or discrepancies in the SPICE model of the rectifier diode and other components used in manufacturing and simulation. These discrepancies can lead to device mismatch and reduced conversion efficiency. Additionally, at low input powers, the diode may not operate in its optimal mode, resulting in a decrease in efficiency. Therefore, in order to improve this deviation, more accurate diode SPICE models can be explored, SMA joints that do not require welding and have low losses can be selected, and the use of connecting wires can be minimized during testing.

Table V compares the performance of our proposed rectenna with similar designs from existing literature. This comparison underscores that our rectenna achieves a broad impedance bandwidth and significantly enhanced conversion efficiency across an extensive frequency range. Notably, the measured conversion efficiency for more than 50% of the frequency fractional bandwidth reaches 28%, markedly surpassing the high conversion efficiency bandwidths reported in other studies. The work referenced in [34] is primarily focused on matching the antenna and circuit to 50 ohms at a single

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	Ref.	Year	Frequency (GHz)	RLBW (%)	Overall complexity	Need impedance matching		Measured efficiency > 50%	Maximum measured efficiency
	[6]	2024	24.6-40	47.6	Complex	Yes	10.75*10.75*8.28	25-37.5 GHz	60%@6 dBm
	[24]	2024	11.5-12.5	8.3	Complex	Yes	NA	≈ 12-12.5 GHz (4%)	≈50%@10 dBm
	[33]	2016	34-36	5.7	Complex	Yes	5.62*4.29*0.15	34.1-34.3 GHz, 34.5-34.8 GHz (0.58%, 0.86%)	63.8% @18.9 dBm
	[34]	2014	24.2-25.8	6.4	Complex	Yes	4.58*3.75*0.12	No	42% @12.6 dBm
	[35]	2024	23.7-24.2 27.7-28.2	2.1/1.8	Complex	Yes	1.9*3.8*0.08	No	49.1/47.8%@ 18 dBm
	[36]	2023	21-27.7	27.9	Complex	Yes	1.6*1.6*0.06	24-24.5 GHz (2%)	63%@15.2 dBm
	[58]	2020	33-38	14	Complex	Yes	NA	NA	67% @19 dBm
	This work	2024	10.95-15.23	33.4	Simple	No	1.65*2.94*0.16	11-14.6 GHz (28%)	61% @18 dBm

 $\label{topological} TABLE\ V$ Comparison Between the Proposed Rectenna and Other Works

RLBW: -10 dB Return loss bandwidth. NA: Not available.

frequency of 24 GHz before integrating them. This approach introduces numerous matching branches at the circuit end, increasing both the complexity of the rectifier circuit and the physical size of the rectenna.

In contrast, our design targets wider bandwidths and non-50- Ω matching, resulting in a much simpler circuit structure than that described in [34]. Additionally, this article details the use of low-loss substrates and advanced integration methods that not only significantly improve conversion efficiency but also compact the overall structure of the rectenna, making it more suitable for IoT applications. Furthermore, compared to LP receiving antennas, the CP receiving antenna in our design more effectively addresses issues of multipath distortion and polarization mismatch.

Ultimately, this design maintains a simple structure and high conversion efficiency while keeping a low profile, providing a distinct advantage over rectenna designs that rely on antenna arrays.

V. CONCLUSION

In this article, we propose a co-design strategy for a wideband CP, simple structure and high-efficiency rectenna operating at 10–20-GHz frequencies. The rectenna integrates a wideband CP ME-dipole antenna and a wideband rectifier, achieving CP through a metal strip that generates two LP ME-dipole modes with a phase difference. Complementary source technology further enhances the antenna's bandwidth, resulting in a measured impedance bandwidth of 36.8% (11.65–16.9 GHz), a 3-dB ARBW of 29.54% (12.55–16.9 GHz), and a peak gain of 8.5 dBic. The rectenna also exhibits a wide measured impedance bandwidth of 33.4% (11.05–15.4 GHz) and a peak RF–DC conversion efficiency of 61%. This design

eliminates the need for separate impedance matching circuits, ensuring excellent impedance matching and mitigating issues like multipath distortion and polarization mismatch. The rectenna's wide bandwidth, simple structure, compact, high efficiency, and broad beam make it ideal for IoT applications, such as powering smart home sensors, implantable medical devices, industrial IoT sensors, and wearable technology, enhancing performance and scalability in diverse and dynamic environments.

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